

ABSTRACT

An apparatus and method for providing control signals, comprising a processor coupled to a memory, for generating a signal sequence. The processor encodes the signal sequence into a plurality of symbols using a 5 pulse position modulated (5PPM) scheme. Advantageously, the last position of each symbol encoded under the 5 PPM scheme is set to a low bit. The symbol sequence represents a signaling protocol having a payload portion and a header portion, wherein the header portion comprises a plurality of fields for defining the payload portion. The payload portion indicates keyboard characters or coordinates for a pointing device.

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